

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	70177	power adj2 amplifier	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/08/11 18:18
2	BRS	L2	819203	bias\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/08/11 18:18
3	BRS	L3	86819	adjust\$6 near5 power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/08/11 18:18
4	BRS	L4	532915	control\$6 near5 power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/08/11 18:18
5	BRS	L5	150643	ramp\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/08/11 18:18
6	BRS	L6	5313	dual-gate or (dual adj1 gate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/08/11 18:18
7	BRS	L7	740181	transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/08/11 18:18
8	BRS	L8	1	1 and 2 and 3 and 4 and 5 and 6 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/08/11 18:19

	Type	L #	Hits	Search Text	DBs	Time Stamp
9	BRS	L9	250	1 and 2 and 3 and 4 and 5 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/08/11 18:19